

U.S. PTO  
10/23/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10023723		FILING DATE 12/21/2001		CLASS 99	SUBCLASS 257	GAU 778	EXAMINER Galt
<b>**APPLICANTS:</b> Chandran Biju, Gonzalez Carlos,							
<b>**CONTINUING DATA VERIFIED:</b>							
<b>** FOREIGN APPLICATIONS VERIFIED:</b>							
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>				RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiner's initials				ATTORNEY DOCKET NO 219.40780X00			
TITLE : Semiconductor package with low resistance package-to-die interconnect scheme for reduced die stresses							

U.S. DEPT. OF COMM. PAT. & TM. PTO-CR/L/Rev. 12-04

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
<b>ISSUE FEE</b>		Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>	
Amount Due	Date Paid	<b>DRAWING</b>	
		Sheets Drawg.	Pipe Drawg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
<input type="checkbox"/> <b>TERMINAL</b> <b>DISCLAIMER</b>		Primary Examiner: Application Examiner:	
<b>PREPARED FOR ISSUE</b>		<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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